



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

KANDA

Serial No. 09/607,219

Filed: June 30, 2000

For: SEMICONDUCTOR DEVICES AND
METHODS OF FABRICATING
THE SAME

Group Art Unit: 2814

Examiner: Quach, T.

2814
R5-18/5053
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TECHNOLOGY CENTER
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J. F. Quach

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sirs:

In response to the Election/Restriction Requirement dated Sept. 10, 2001, please enter and consider the following.

Applicant hereby elects to prosecute Group II, claims 22-43, drawn to processes of making a semiconductor device.

Examination on the merits is respectfully requested. It is believed that no fees are due in connection with this paper. If however, any such fees are due, the Commissioner is hereby authorized to charge payment of the fees associated with this paper to Deposit Account No. 50-0585. A copy of this paper is enclosed.

Respectfully submitted,

Alan S. Raynes

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Reg. No. 39,809

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Dated: October 10, 2001

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on October 10, 2001.

Alan S. Raynes

Alan S. Raynes

Oct. 10, 2001

Date